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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Geoff W. Taylor, et al.

Group Art Unit: 2874

Serial No.: 10/602,217

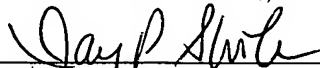
Examiner: Sung H Pak

Filed: June 24, 2003

Attorney Docket: OPE-024

Title: Photonic Sigma Delta Analog-to-Digital Conversion Employing Dual Heterojunction Thyristors

I hereby certify that this correspondence is being deposited on this day with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450.

  
Jay P. Sbrollini  
Reg. No. 36,266

3/20/2006  
Date

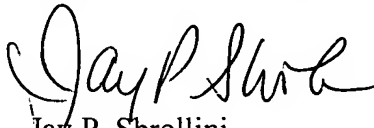
Honorable Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

SUBMITTAL OF FORMAL DRAWINGS

Enclosed herewith are formal drawings that are believed to comply with the rules and regulations of the Patent Office. If, for any reason, the Official Draftsman should object to any of the drawings, please notify the undersigned immediately so that extension fees and additional delay can be avoided. This submittal is being made in a timely fashion.

Respectfully submitted,

  
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